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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

10/049971

APPL NUM 10049971	FILING DATE 02/12/2002	CLASS 423	SUBCLASS 17	GAU 1754	EXAMINER 115099
**APPLICANTS: Komiya Satoshi; Yoshino Shiro; Danbata Masayoshi; Hayashida Kouichirou; <div style="text-align: center;">117</div>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/04216 06/26/2000 (M)					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 241187/1999 08/27/1999 (M)					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials (M)				ATTORNEY DOCKET NO 86396	
TITLE : Epitaxial silicon wafer				U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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